

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet	1	of	2
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Complete if Known

Application Number	10/090,944
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Filing Date	March 4, 2002
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First Named Inventor	Paul E Luse
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Art Unit	2818 2189
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Examiner Name	Not yet assigned PEI KAR.
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Attorney Docket Number	42390P13502
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U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

**Examiner
Signature**

Date	
Considered	

5/4/05

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Application Number	10/090,944
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First Named Inventor	Paul E Luse
Art Unit	2818-2189
Examiner Name	Not yet assigned PE/KAR
Attorney Docket Number	42390P13502

NON PATENT LITERATURE DOCUMENTS

[illegible]

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Sheet	1	of	4
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Complete if Known

Application Number	10/090,944
Filing Date	March 4, 2002
First Named Inventor	Paul E Luse
Art Unit	2818
Examiner Name	Not yet assigned
Attorney Docket Number	42390P13502

U.S. PATENT DOCUMENTS

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Application Number	10/090,944
Filing Date	March 4, 2002
First Named Inventor	Paul E Luse
Art Unit	2818
Examiner Name	Not yet assigned
Attorney Docket Number	42390P13502

Sheet	2	of	4
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Substitute for form 1448A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Application Number	10/099,944
Filing Date	3.4.02
First Named Inventor	Paul Luse
Art Unit	
Examiner Name	
Attorney Docket Number	42P13502

Sheet

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of

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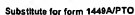
Examiner Initials*	Cite No.*	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T†
	1	Adaptec - Adaptec SCSI RAID 2000S Support	
	2	Adaptec - Adaptec SCSI RAID 2005S Support	
	3	Adaptec - Motherboard Partners and Compatibility	
	4	Adaptec - Adaptec SCSI RAID 2005S Technical Specs	
	5	Adaptec - Adaptec Article: Zero Channel RAID Frequently Asked Questions	
	6	Intel Corp., Product Brief, Intel Server Board SCB2	
	7	Serial ATA: High Speed Serialized AT Attachment Revision 1.0, 29 August 2001	
	8	Super0, Super P3TDDR, User's Manual, Revision 1.0a	
	9	PCI Local Bus, PCI BIOS Specification, Revision 2.1 August 26, 1994	
	10	PCI Local Bus, PCT-to-PCI Bridge Architecture Specification, Revision 1.1, December 16, 1998	
	11	PCI Local Bus, PCI Local Bus Specification, Revision 2.2, December 18, 1998	
	12	PCI Local Bus, Mini PCI Specification, Revision 1.0, October 25, 1999	
	13	PCI Local Bus, PCI-X Addendum to the PCI Local Bus Specification, Revision 1.0a, July 24, 2000	
	14	PCI Local Bus, PCI Local Bus Specification, Revision 2.3, March 29, 2002	

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Application Number	10/090,944
Filing Date	3.4.02
First Named Inventor	Paul Luce
Art Unit	
Examiner Name	
Attorney Docket Number	42013502

Sheet 4 of 4

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